

FIG. 1 (RELATED ART)

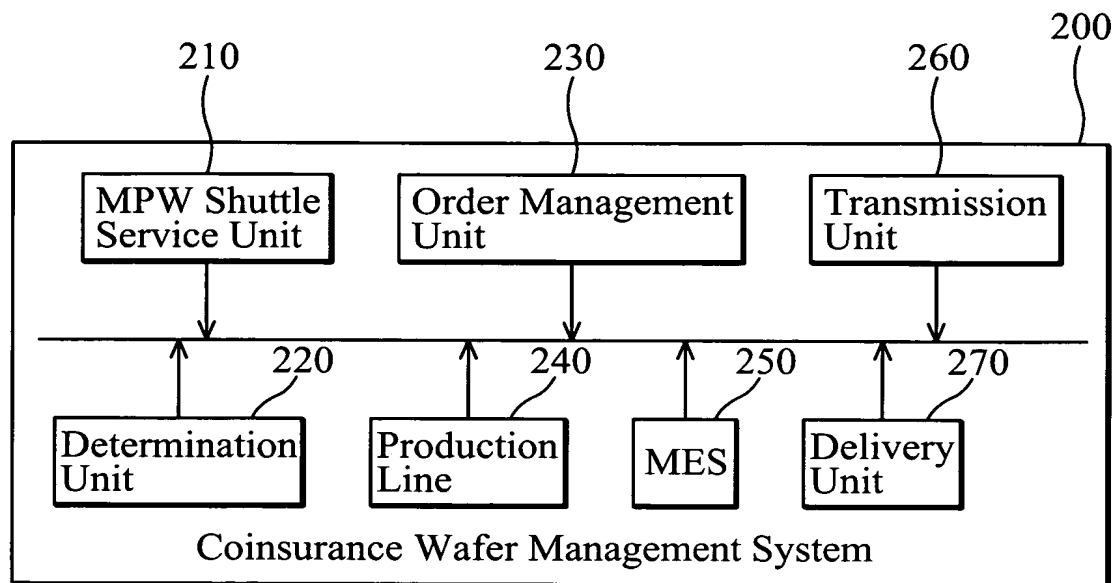


FIG. 2

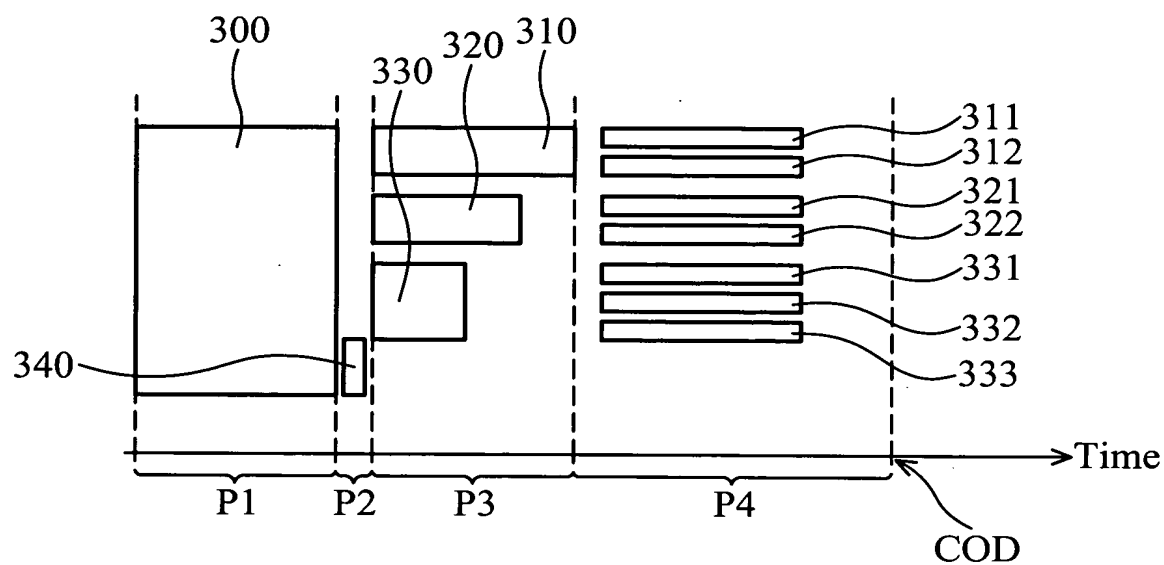


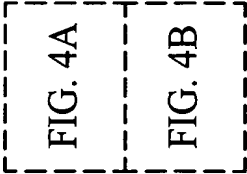
FIG. 3

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| Customer Code | Device Name | Lot ID | Wafer ID | Technology | Fab | Tape-in Date |
|---------------|-------------|--------|-------------------------------|------------|-----|--------------|
| A023 | TMB934_C15 | D62600 | 05;06;07;08;09;10;11;12;13;14 | 0.18 | 4A | 6/5/2001 |
| A458 | TMB934_C11 | D62600 | 05;06;07;08;09;10;11;12;13;14 | 0.18 | 4A | 6/5/2001 |
| E288 | TMB934_C3 | D62600 | 03;04 | 0.18 | 4A | 6/5/2001 |
| J181 | TMB934_C5 | D62600 | 05;06;07;08;09;10;11;12;13;14 | 0.18 | 4A | 6/5/2001 |
| U363 | TMB934_C7 | D62600 | 05;06;07;08;09;10;11;12;13;14 | 0.18 | 4A | 6/5/2001 |
| U376 | TMB934_C4 | D62600 | 03;04 | 0.18 | 4A | 6/5/2001 |
| U446 | TMB934_C8 | D62600 | 01;02 | 0.18 | 4A | 6/5/2001 |
| U583 | TMB934_C9 | D62600 | 05;06;07;08;09;10;11;12;13;14 | 0.18 | 4A | 6/5/2001 |
| U706 | TMB934_C2 | D62600 | 05;06;07;08;09;10;11;12;13;14 | 0.18 | 4A | 6/5/2001 |
| U759 | TMB934_C6 | D62600 | 05;06;07;08;09;10;11;12;13;14 | 0.18 | 4A | 6/5/2001 |
| U800 | TMB934_C12 | D62600 | 03;04 | 0.18 | 4A | 6/5/2001 |

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FIG. 4A



| Process | #P#M | User Type | Committed Date | Coinsurance Wafer ID |
|---------|-------|-----------|----------------|----------------------|
| MiM | 1P6M+ | Customer | 9/5/2001 | 15;16 |
| MiM | 1P6M+ | Customer | 9/5/2001 | 15;16 |
| MiM | 1P6M+ | Customer | 9/10/2002 | 15;16 |
| MiM | 1P6M+ | Customer | 9/5/2001 | 15;16 |
| MiM | 1P6M+ | Customer | 9/5/2004 | 15;16 |
| MiM | 1P6M+ | Customer | 9/10/2002 | 15;16 |
| MiM | 1P6M+ | Customer | 9/15/2002 | 15;16 |
| MiM | 1P6M+ | Customer | 9/5/2007 | 15;16 |
| MiM | 1P6M+ | Customer | 9/5/2007 | 15;16 |
| MiM | 1P6M+ | Customer | 9/5/2007 | 15;16 |
| MiM | 1P6M+ | Customer | 9/10/2002 | 15;16 |

FIG. 4B

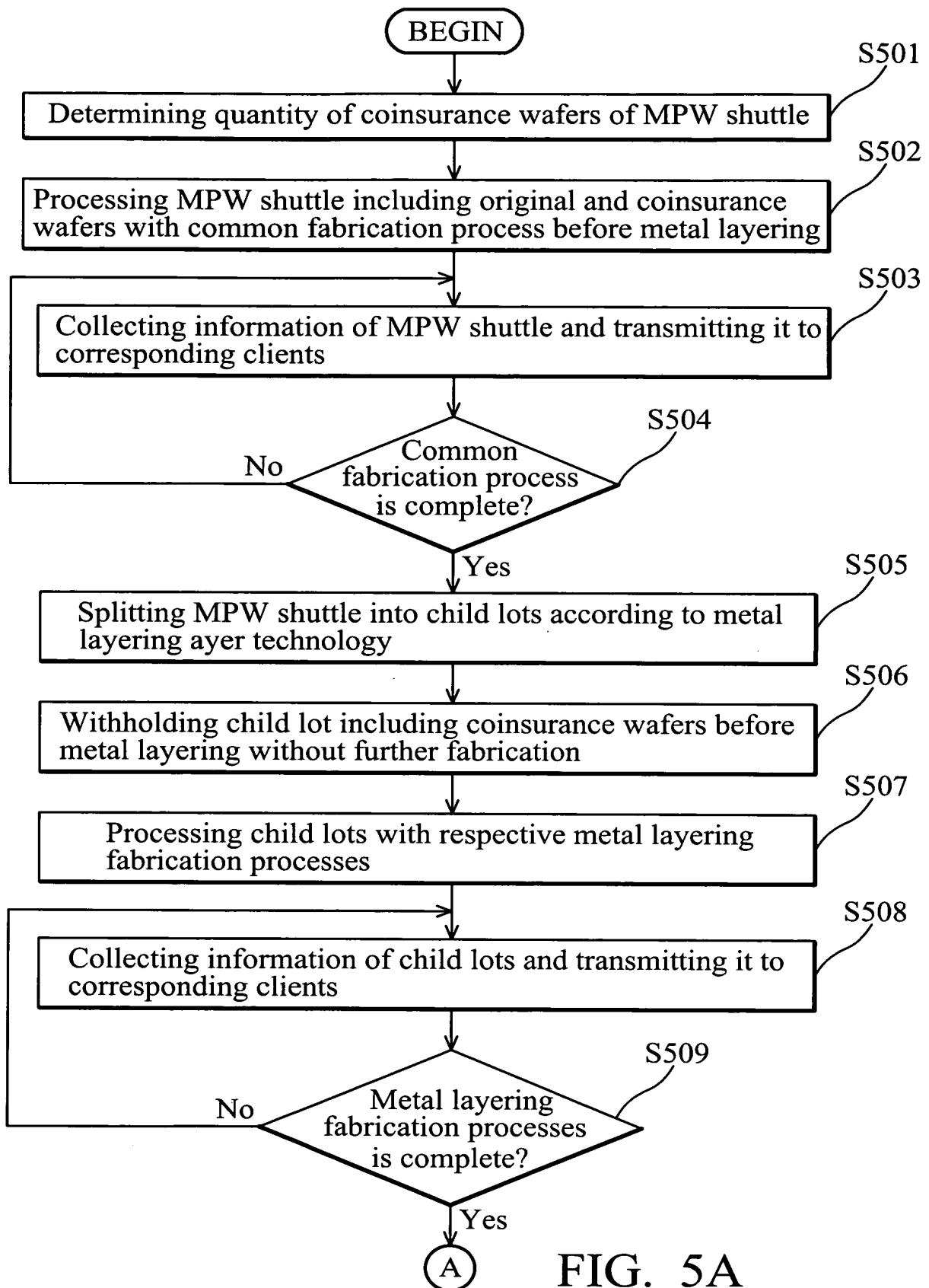


FIG. 5A

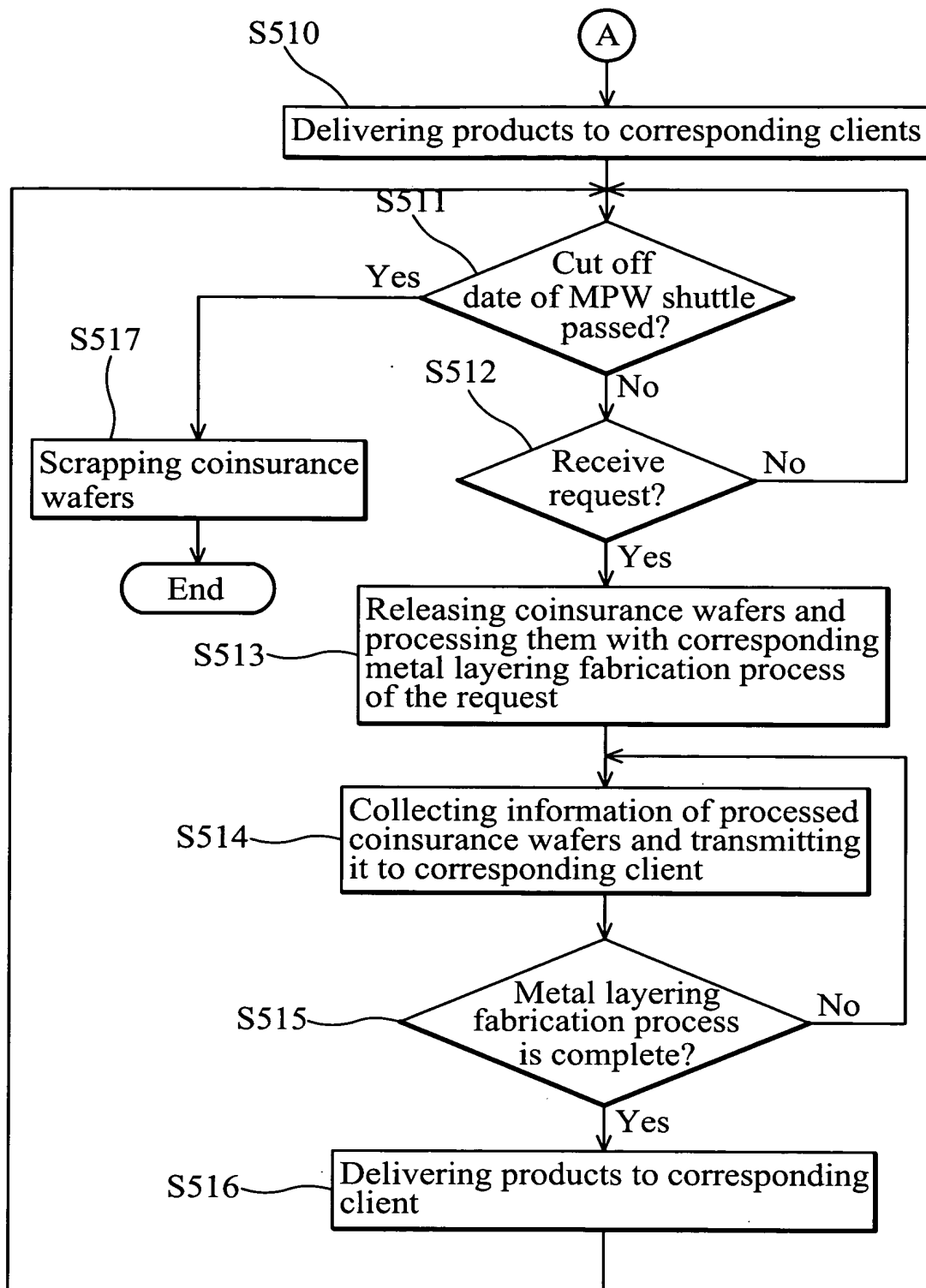


FIG. 5B

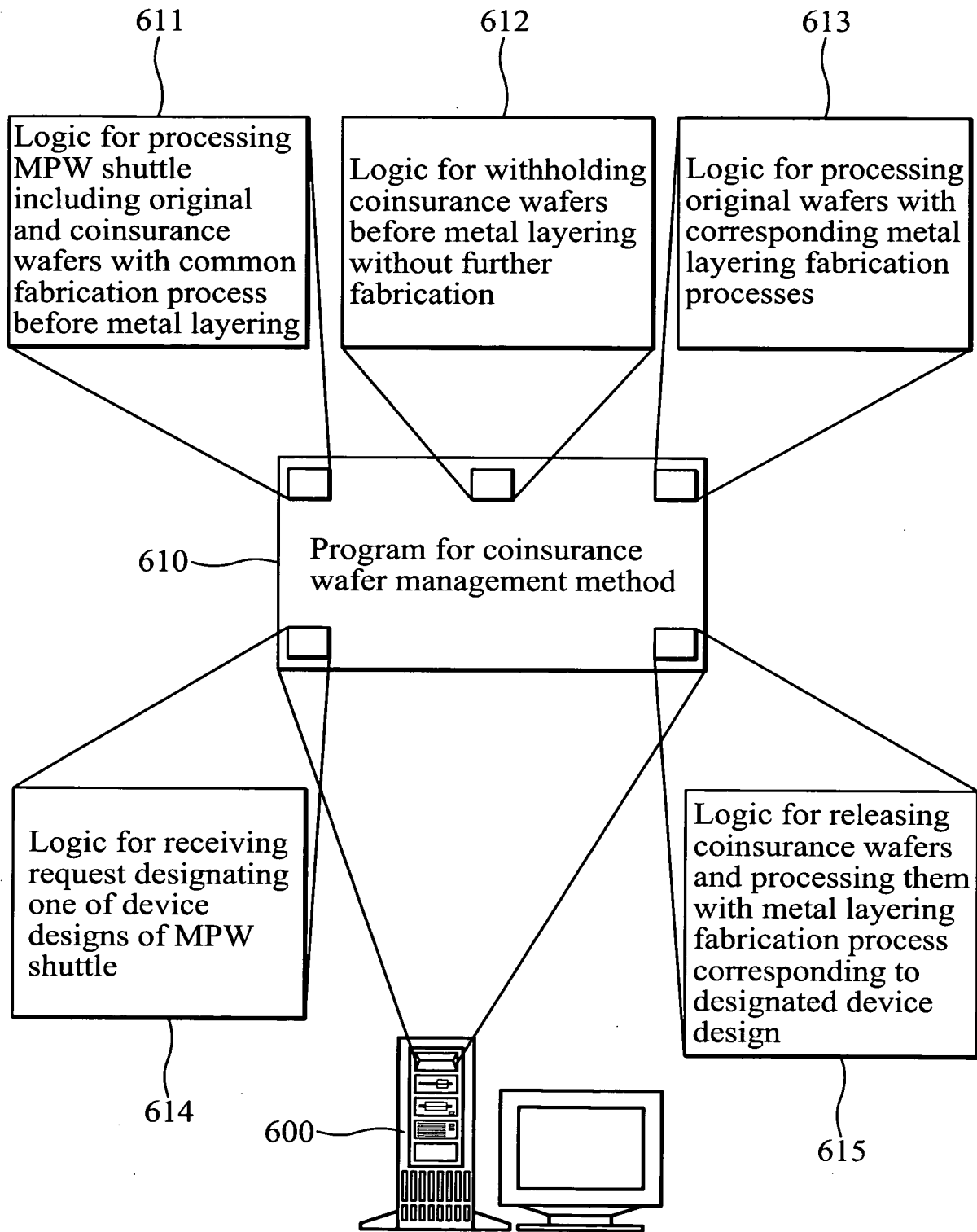


FIG. 6